rakon

RIT3225B

The RIT3225B employs an analogue ASIC for the oscillator and a high order temperature compensation circuit in a 3.2 x 2.5 mm size package. This SMD Temperature Compensated Crystal Oscillators provides voltage control option with a wide frequency ranges available from 9.5 MHz to 52 MHz. It is built using rakon's proprietary processes specifically targeted at high performance GNSS applications.

Features

- Frequency slope and perturbation specifications can be customized to the application requirement
- Excellent phase noise performance
- Standard temperature stability choices are ±0.5, ±1.0, ±1.5 and ±2.5 ppm over temperature from -40 to 85°C

Applications

- o GNSS
- Smartphone 0 o PND
- Consumer
- Communications 0 0

Wi-Fi

3.2 x 2.5 mm



Standard Specifications

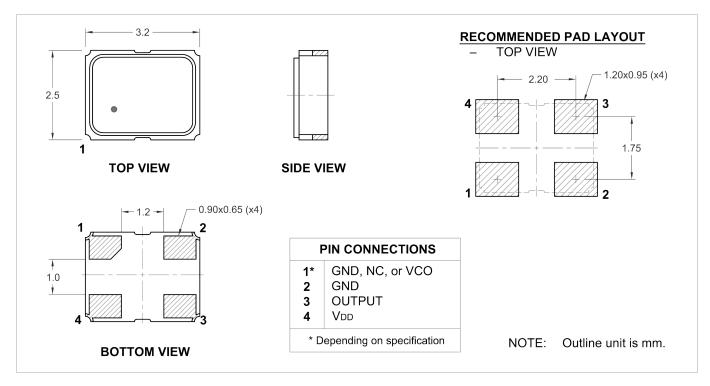
Parameter	Min.	Тур.	Max.	Unit	Test Condition / Description
Nominal frequency		9.5 – 52		MHz	
Frequency calibration			±2	ppm	Offset from nominal frequency measured at 25°C ±2°C
Operating temperature range	-40		85	°C	The operating temperature range over which the frequency stability is measured
Frequency stability over temperature			±0.5 – ±2.5	ppm	Referenced to the midpoint between minimum and maximum frequency value over the specified temperature range ¹ Control voltage set to midpoint of Vc
Sensitivity to supply voltage variations			±0.2	ppm	V_{DD} varied ±5% at 25°C
Sensitivity to load variations			±0.1	ppm	$\pm 10\%$ load change at 25°C ²
Long term stability			±1	ppm	Frequency drift over 1 year at 25°C
Supply voltage (V _{DD})		1.8 - 3.3		V	With a tolerance of ±5%
Supply current			6	mA	At maximum V _{DD} ²
Output voltage level	0.8			V	At minimum $V_{\text{DD, s}}\text{pecified}$ for load stated in oscillator output section at 25°C 2
Output waveform					DC coupled clipped sinewave ³

¹ Parts should be shielded from drafts causing unexpected thermal gradients. Temperature changes due to ambient air currents on the oscillator can lead to short term frequency drift.

² Specified for load stated in oscillator output section at 25°C.

³ External AC-Coupling capacitor required. 1 nF or greater recommended.





Model Outline and Recommended Pad Layout

Test Circuit

